

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

FRANCESCO PESSOLANO ET AL

NL 000667

Serial No.

Filed: CONCURRENTLY

DIGITAL SIGNAL PROCESSING APPARATUS

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

4. (Amended) Apparatus according to claim 2, comprising a register file (8) characterized in that said register file (8) is extended with said FIFO register means (14).

5. (Amended) Apparatus according to claim 2, characterized in that said FIFO register (14) means comprises a plurality of FIFO registers.

6. (Amended) Apparatus according to claim 1, characterized in that each of said functional units (10) are provided with at least one control unit (12).

7. (Amended) Apparatus according to claim 1, which apparatus is adapted to execute a pipeline consisting of a plurality of stages, wherein each stage is executed by a functional unit (10).

8. (Amended) Apparatus according to claim 1, characterized in that for each control unit (12) an instruction register and a counter are provided, where-in said counter indicates the number of times an instruction stored in said instruction register has to be executed by the corresponding functional unit (10).

9. (Amended) Apparatus according to claim 1, further comprising a program memory means (6) storing a main program, characterized in that said main program contains directives for instructing said control units.

13. (Amended) Method according to claim 11, wherein a pipeline consisting of a plurality of stages is provided, and each stage is executed by a functional unit (10).

14. (Amended) Method according to claim 10, characterized in that the number of times an instruction stored has to be executed by a functional unit (10) is counted by the corresponding control unit (12).

REMARKS

The foregoing amendments to claims 4-9 and 13-15 were made solely to avoid filing the claims in the multiple dependent form so as to avoid the additional filing fee.

The claims were not amended in order to address issues of patentability and Applicant respectfully reserves all rights he may have under the Doctrine of Equivalents. Applicant furthermore reserves his right to reintroduce subject matter deleted herein at a later time during the prosecution of this application or continuing applications.

Respectfully submitted,

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APPENDIX

4. (Amended) Apparatus according to claim 2 ~~or 3~~, comprising a register file (8) characterized in that said register file (8) is extended with said FIFO register means (14).

5. (Amended) Apparatus according to claim 2 ~~any one of claims 2 to 4~~, characterized in that said FIFO register (14) means comprises a plurality of FIFO registers.

6. (Amended) Apparatus according to claim 1 ~~at least one of the preceding claims~~, characterized in that each of said functional units (10) are provided with at least one control unit (12).

7. (Amended) Apparatus according to claim 1 ~~at least one of the preceding claims~~, which apparatus is adapted to execute a pipeline consisting of a plurality of stages, wherein each stage is executed by a functional unit (10).

8. (Amended) Apparatus according to claim 1 ~~at least one of the preceding claims~~, characterized in that for each control unit (12) an instruction register and a counter are provided, where-in said counter indicates the number of times an instruction stored in said instruction register has to be executed by the corresponding functional unit (10).

9. (Amended) Apparatus according to claim 1 ~~at least any one of the proceeding claims~~, further comprising a program memory means (6) storing a main program, characterized in that said main program contains directives for instructing said control units.

13. (Amended) Method according to claim 11 ~~or 12~~, wherein a pipeline consisting of a plurality of stages is provided, and each stage is executed by a functional unit (10).

14. (Amended) Method according to claim 10 ~~at least any one of the claims 10 to 13~~, characterized in that the number of times an instruction stored has to be executed by a functional unit (10) is counted by the corresponding control unit (12).